

SPECIFICATIONS

Output Frequency

Frequency:	F_{OUT}		
Frequency stability and accuracy	Same as reference input		
Internal OCXO reference	$\pm 5 \times 10^{-8}$ (over temp range)		
Aging (After 30 days):	$\pm 5 \times 10^{-10}$ (per day)		
Adjustability (typ.):	10 years		
Phase noise in dBc/Hz @ 10 MHz: L(f) < 10 Hz is reference dependent		Typ.	Max.
	L(1 Hz)	-90	
	L(10 Hz)	-125	
	L(100 Hz)	-150	
	L(1 kHz)	-160	
	L(10 kHz)	-165	
	L(100 kHz)	-165	
	L(1 MHz)		
Spurious (max.):	-60 dBc		
Harmonics (typ.):	-40 dBc		
Power out (min.) @ 25°C:	+13 dBm		
Power variation (freq. & temp.) (max.):	2 dB		
Load VSWR (max.):	2:1		
Phase-lock indicator (LD), High = lock:	Open collector (note 2)		

Reference Frequency

Input reference frequency:	F_{IN}
Input level:	0 dBm ± 3 dB

DC Power

+15.0 Vdc $\pm 5\%$	Typical:	200 mA
	Warm-up:	400 mA

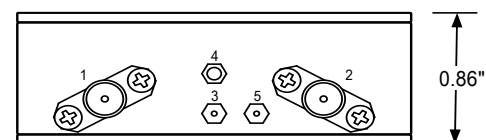
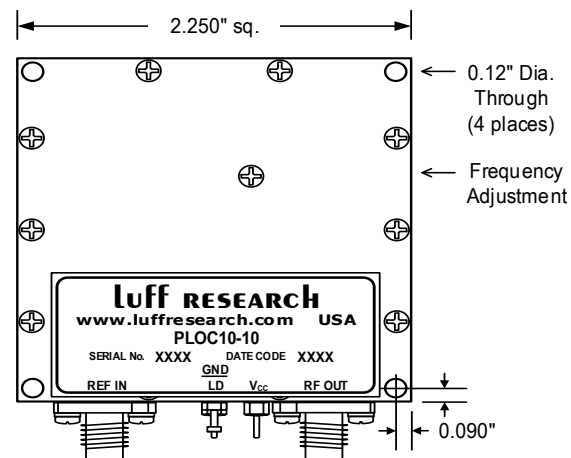
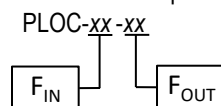
Environment

Operating temperature range (surface):	-20°C to 70°C
Storage temperature range:	-40°C to 85°C
Relative humidity (non-condensing):	90%RH @ 40°
Shock:	30 G / 10msec
Vibration:	4 G / 20 Hz - 20 kHz
Specifications @:	+25°C

Block Diagram



4. Part No. Description



1. Reference Input
2. RF Output
3. Lock Detector
4. Ground Lug
5. DC Power (Vcc)

Notes:

1. The output signal is generated by a precision OCXO which is phase-locked to the reference input. When the reference input is not present the internal OCXO acts as a stand alone precision source.
2. LD Output: Standard is open collector. For TTL add -T at the end of the model number.
3. Any input / output configuration is possible.

F_{IN} (in MHz)	F_{OUT} (in MHz)
5	5
10	10
10.23	10.23
50	50
80	80
100	100



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PRODUCT DATA SHEET

Frequency Standard Phase Noise Clean-up PLO

Model: PLOC-xx-xx

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